

**SPECIFICATION
OF
LCD MODULE**

MODULE NO.: HL043T31C-04

Customer Approval:

☐ **Accept**

☐ **Reject**

	SIGNATURE	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

DOCUMENT REVISION HISTORY

Sample Version	Doc. Version	DATE	DESCRIPTION	CHECKED BY
	A0	2022-9-5	First Release.	
	A1	2023-11-16	Modify view angel	
	A2	2023-11-22	Modify LCD to IPS	

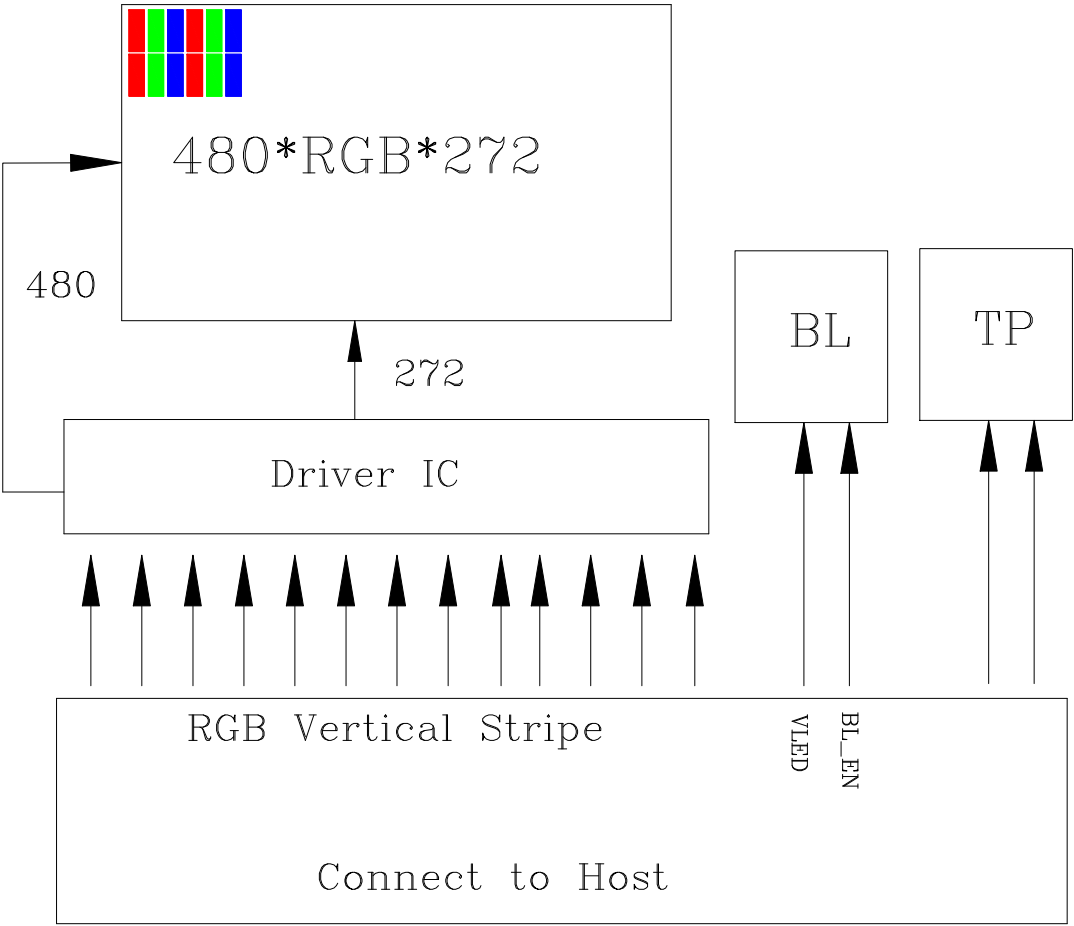
1. MECHANICAL SPECIFICATIONS:

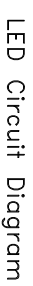
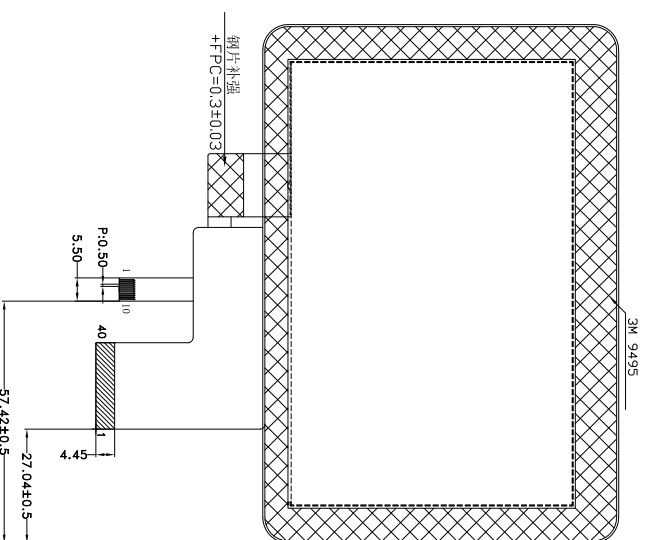
ITEM	SPECIFICATION	UNIT
OUTLINE DIMENSIONS	123.04 (W) X84.46(H) X4.8(D)	mm
DISPLAY SIZE	4.3	inch
DOT PITCH	0.198mmX0.198mm	mm
NUMBER OF DOTS	480* (RGB) *272	-
DRIVER IC	ILI6485	-
CTP DRIVER IC	FT5346	
LCD TYPE	TFT(16.7M) TRANSMISSIVE	-
BACKLIGHT TYPE	LED White	-
VIEWING DIRECTION	85/85/85/85	-
TOUCH PANEL	Capacitive Touch Panel	

***See attached drawing for details.**

**** IIC address : 0x70, 7bit_addr: 0x38**

2.BLOCK DIAGRAM:





HL 海罗光电有限公司
Hello Lighting co.,LTD

HL043T31C-04

1	NC
2	NC
3	RST
4	GND
5	INT
6	SDA
7	SCL
8	GND
9	GND
10	VDD

CTP CON ASSIGNMENT:

4.0	Modify LCD to IPS	20231122	TITLE: LCM DIMENSION		REV. : 2.0	SCALE : -
3.0	Modify Modify view angle	20231116	DESIGN		PROJECTOR: 	
2.0	Modify CTP FPC, BL LED	20190606	CHECKED			
1.0	First version		APPROVED			
MARK REV.	CONTENTS MODIFIED	DATE				

3.DIMENSIONAL

4. PIN DESCRIPTION:

-FPC connector: FH19SC-40S-0.5SH

NO.	PIN NAME	I/O	Description
1	VLED-	P	Backlight LED Cathode
2	VLED+	P	Backlight LED Anode
3	GND	P	System Ground
4	VCC	P	Power supply for logic operation
5	R0	I	Red data input(LSB)
6	R1	I	Red data input
7	R2	I	Red data input
8	R3	I	Red data input
9	R4	I	Red data input
10	R5	I	Red data input
11	R6	I	Red data input
12	R7	I	Red data input(MSB)
13	G0	I	Green data input(LSB)
14	G1	I	Green data input
15	G2	I	Green data input
16	G3	I	Green data input
17	G4	I	Green data input
18	G5	I	Green data input
19	G6	I	Green data input
20	G7	I	Green data input(MSB)
21	B0	I	Blue data input(LSB).
22	B1	I	Blue data input
23	B2	I	Blue data input
24	B3	I	Blue data input
25	B4	I	Blue data input
26	B5	I	Blue data input
27	B6	I	Blue data input
28	B7	I	Blue data input(MSB)
29	GND	P	Ground
30	CLK	I	Pixel clock signal ,in RGB mode
31	DISP	I	Display on/off control
32	HSYNC	I	Horizontal Sync signal in RGB mode
33	VSYNC	I	Vrtical Sync signal
34	DEN	I	Data Enable
35	NC	-	No connect
36	GND	P	System Ground
37	NC	-	No connect
38	NC	-	No connect
39	NC	-	No connect
40	NC	-	No connect

CTP pin define

- FPC connector: FH19SC-10S-0.5SH

NO.	PIN NAME	I/O	Description
1	NC	-	No connection
2	NC	-	No connection
3	RST	I	CTP reset signal input
4	GND	P	Power ground
5	INT	O	CTP interrupt output
6	SDA	IO	CTP IIC data
7	SCL	I	CTP IIC clock
8	GND	P	Power ground
9	GND	P	Power ground
10	VDD_CTP	P	Power supply

Note:

O: LCD to host

I: host to LCD

IO: LCD to host or host to LCD

P: power

5. MAXIMUM ABSOLUTE LIMIT:

Item	Symbol	Value	Unit
Power supply voltage for LCD	V _{CC}	-0.3~3.3	V
Power supply voltage for CTP	VDD_CTP	-0.3~3.3	V
Input voltage	V _{in}	V _{DD} +0.3	V
Operating temperature	T _{opr}	-20 to 70	°C
Storage temperature	T _{stg}	-30 to 80	°C

Note: Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken.

They do not assure operations.

Note2: Background color changes slightly depending on ambient temperature. This Phenomenon is reversible.

T_a ≤ 70°C: 75%RH max

T_a > 70°C: absolute humidity must be lower than the humidity of 75%RH at 70°C

Note3: T_a at -30°C will be <48hrs, at 80 °C will be <120hrs

6.ELECTRICAL CHARACTERISTICS

6-1 DC Characteristics (T_a=25°C)

Item	Symbol	Min	Type	Max	Unit	Test condition
Operating voltage for LCD	V _{CC}	3.0	3.3	3.6	V	-
Operating voltage for CTP	VDD_CTP	3.0	3.3	3.6	V	-
Supply current	I _{DD}	-	TBD	-	mA	
Input voltage	V _{IH}	0.7VDD	-	VDD	V	-
	V _{IL}	0	-	0.3VDD	V	
Input leakage current	I _{IL}	-1.0	-	1.0	μA	V _{IN} =V _{DD} or V _{SS}

Note: Voltage greater than above may damage the module.

All voltages are specified relative to V_{SS}=0V.

6-2 Backlight Electrical-optical Characteristics

1. Stander Lamp Styles(Edge Lighting Type):

The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:

2. The Main Advantages of the LED Backlight are as following:

2.1 The brightness of the backlight can simply be adjusted by a resistor or a potentiometer.

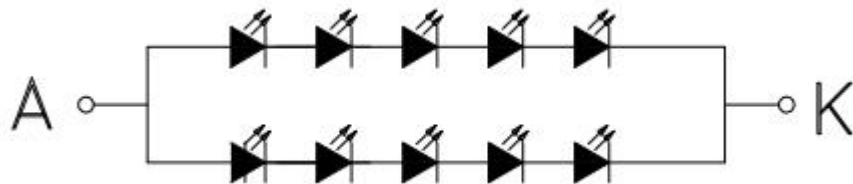
3. Data About LED Backlight:

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition	Note
Supply Voltage	Vf	-	15	16	V	If=40 mA	-
Supply Current	If	-	40	-	mA	-	-
Reverse Voltage	Vr	-	-	5	V	10uA	
Power dissipation	Pd	-	600	-	mW	-	
Uniformity for LCM	-	80	-	-	%	If=40mA	3
Life Time	-	30000	40000	-	Hr	If=40 mA	-
Backlight Color	White						

1. Average Luminous Intensity of P1-P9

2. Uniformity = Min/Max * 100%

3.LED life time defined as follows: The final brightness is at 50% of original brightness



6-3.Touch screen panel specifications:

Item	Symbol	Min.	Type.	Max.	unit	Remark
TransParency	-	-	85	-	%	JIS K-7105
Hardness Of Surface	-	-	6	-	H	150gf 45°
FPC Peeling Strength	-	5		-	N	Upward 90°

7.AC TIMING

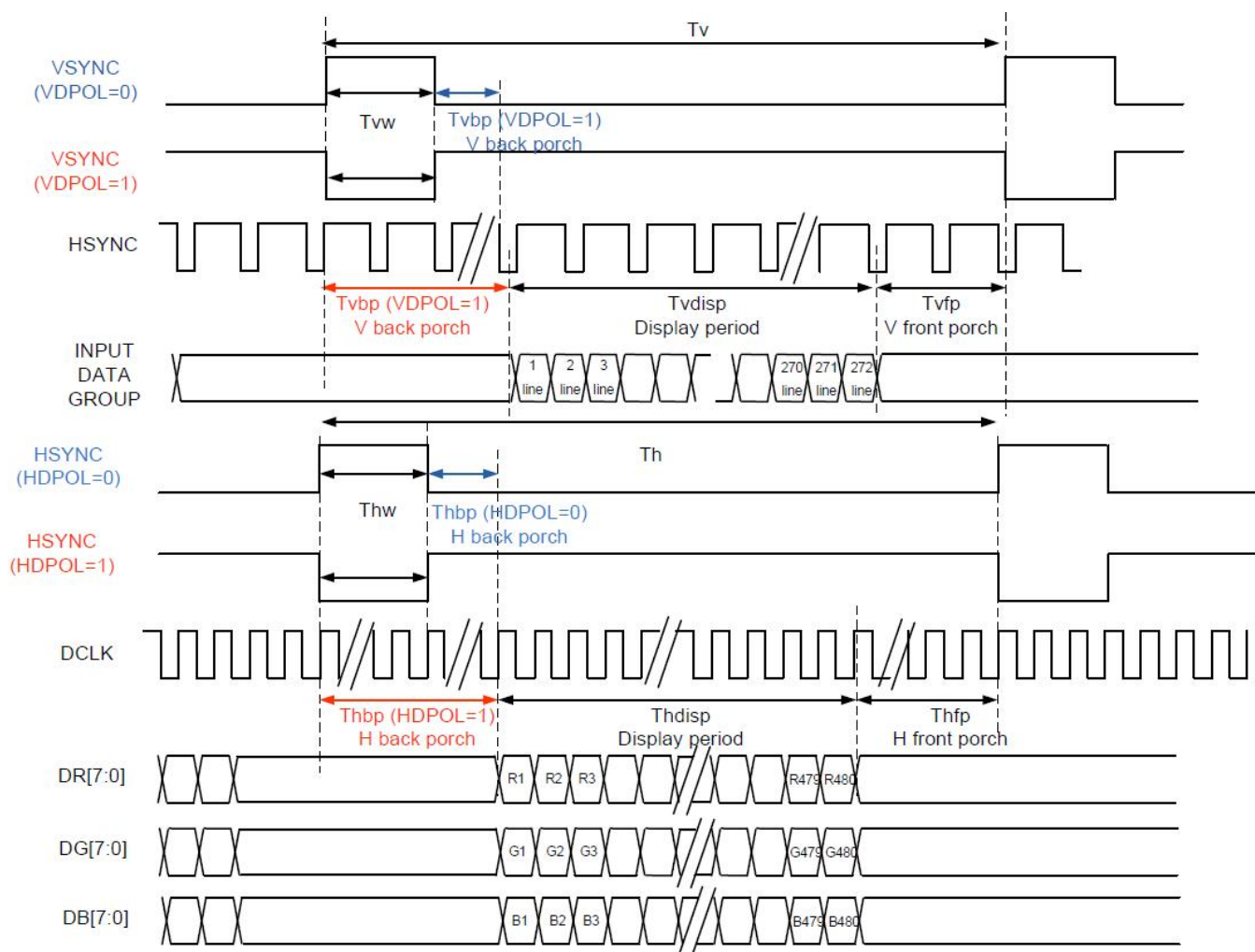
7-1 AC input timing

7-1-1 input setup timing parameter setting(24bit RGB)

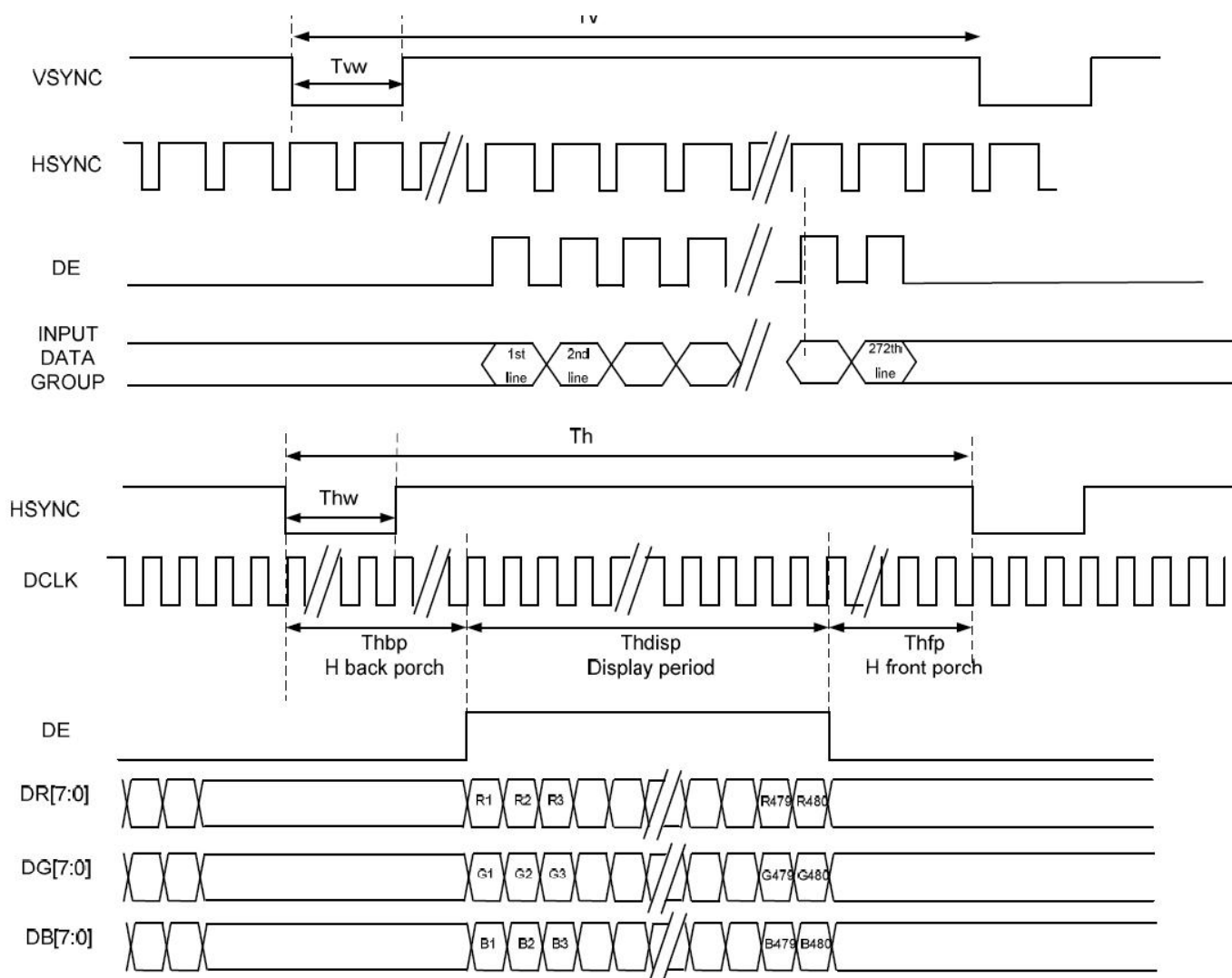
Item		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency		Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	75	DCLK	
VSYNC	Period Time	Tv	276	292	321	H	
	Display Period	Tvdisp		272		H	
	Back Porch	Tvbp	2	12	12	H	By V_Blanking setting
	Front Porch	Tvfp	2	8	37	H	
	Pulse Width	Tvw	2	4	37	H	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

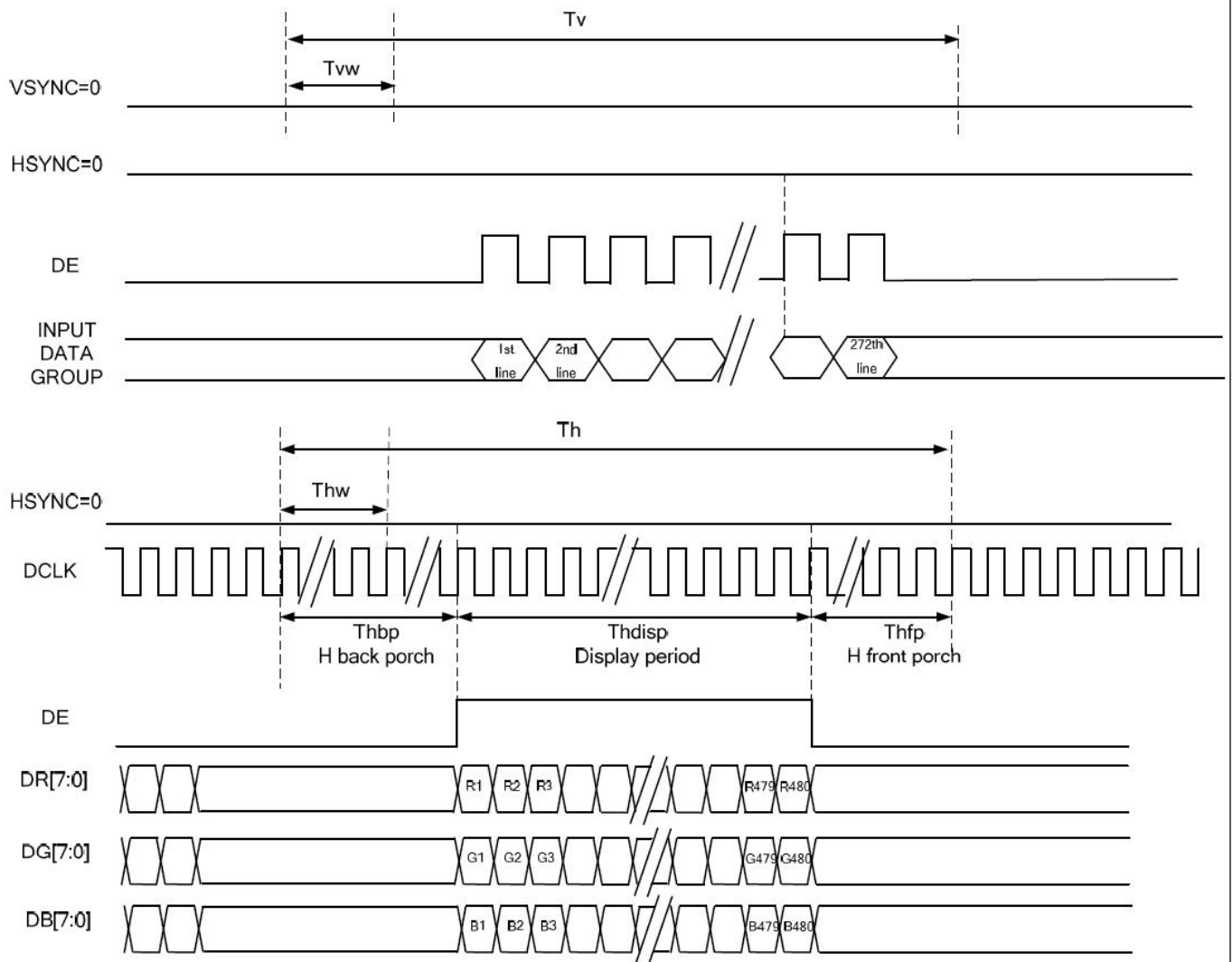
7-2 SYNC Mode Timing Diagram



7-3 SYNC-DE Mode Timing Diagram

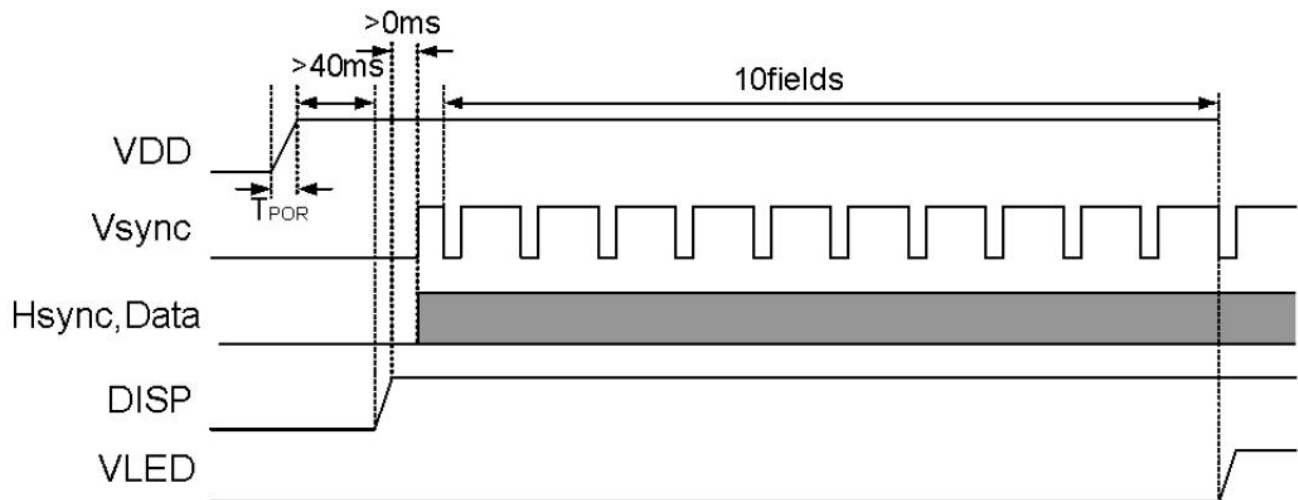


7-4 DE Mode Timing Diagram

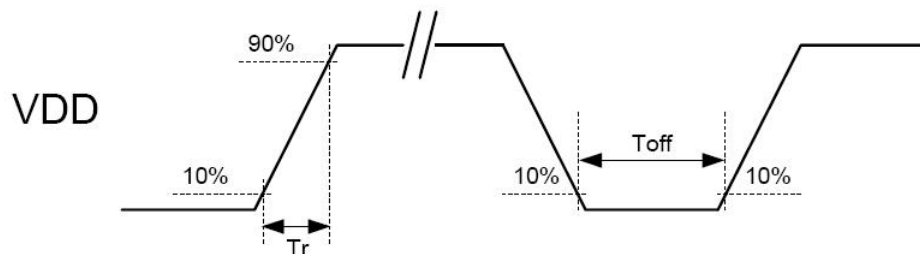
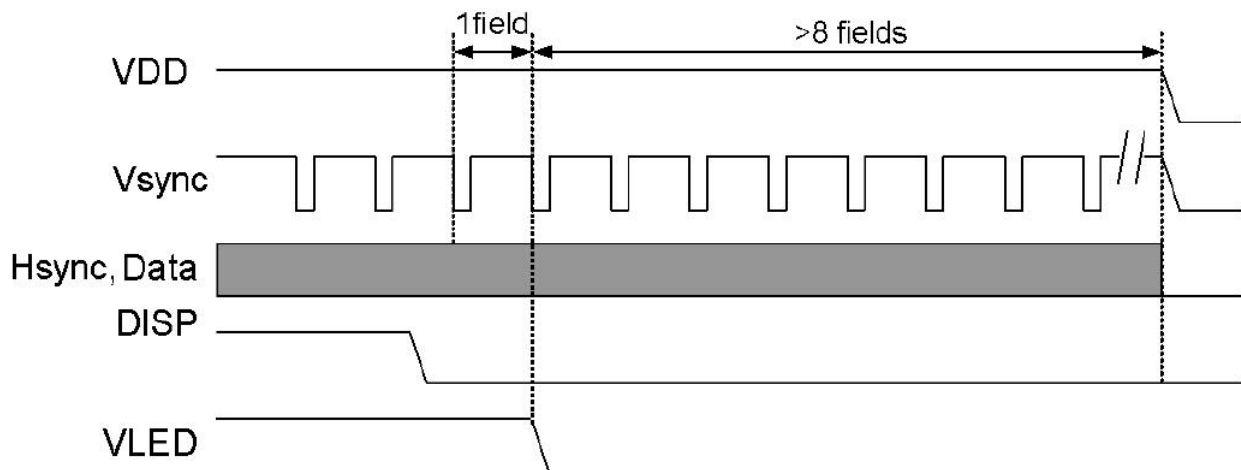


7-5 Power ON/OFF Sequence

7-5-1 Power On Sequence



7-5-2 Power OFF Sequence



VDD power input timing

Notes:

Data include R0~R7, G0~G7, B0~B7, HSD, VSD, DCLK, DE

Power on sequence: VDD $\rightarrow$ DISP $\rightarrow$ Data $\rightarrow$ V_{LED}

Power off sequence: DISP $\rightarrow$ V_{LED} $\rightarrow$ Data $\rightarrow$ VDD

VDD power input timing: $0.5\text{ms} < T_r < 10\text{ms}$; $T_{off} > 500\text{ms}$

8. OPTICAL CHARACTERISTICS:

Driving the backlight

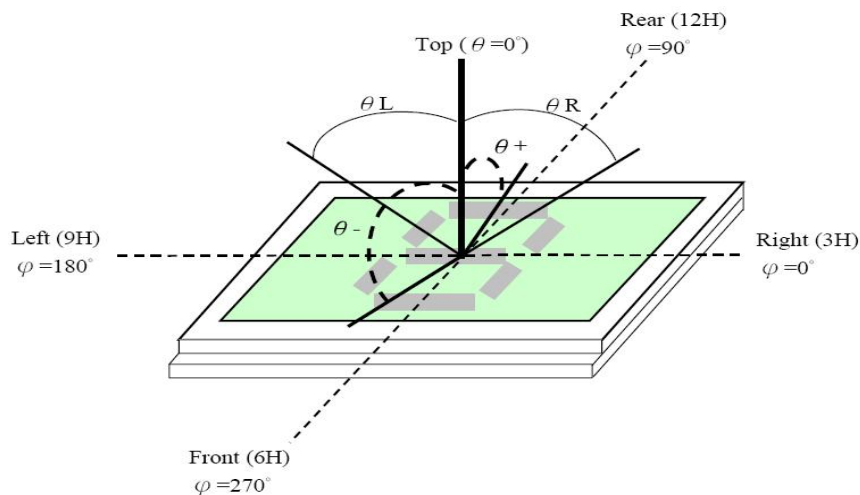
No.	ITEM		Symbol	Conditions	Specification			Unit	Note
					Min	Typ	Max		
1	Response Time		Tr+Tf	25℃	-	30	35	Ms	(1)(2)
2	Contrast Rate		Cr	θ=0, Normal viewing angle	800	1000	-	-	(1)(3)
3	Viewing Angle	Hor.	θL	CR>10	80	85	-	Deg	-
			θR		80	85	-		
		Ver.	Θ+		80	85	-		
			Θ-		80	85	-		
4	Chromaticiry	White	x	Brightness is ON	0.275	0.305	0.335		
			y		0.306	0.336	0.366		
		Red	x		0.573	0.603	0.633		
			y		0.277	0.307	0.337		
		Green	x		0.287	0.314	0.344		
			y		0.527	0.557	0.587		
		Blue	x		0.115	0.145	0.175		
			y		0.123	0.153	0.183		
5	NTSC		S		50		%		
6	luminance		L		240	300	cd/m2	With TP	
7	Uniformity		U		80		%		

Measure Conditions:

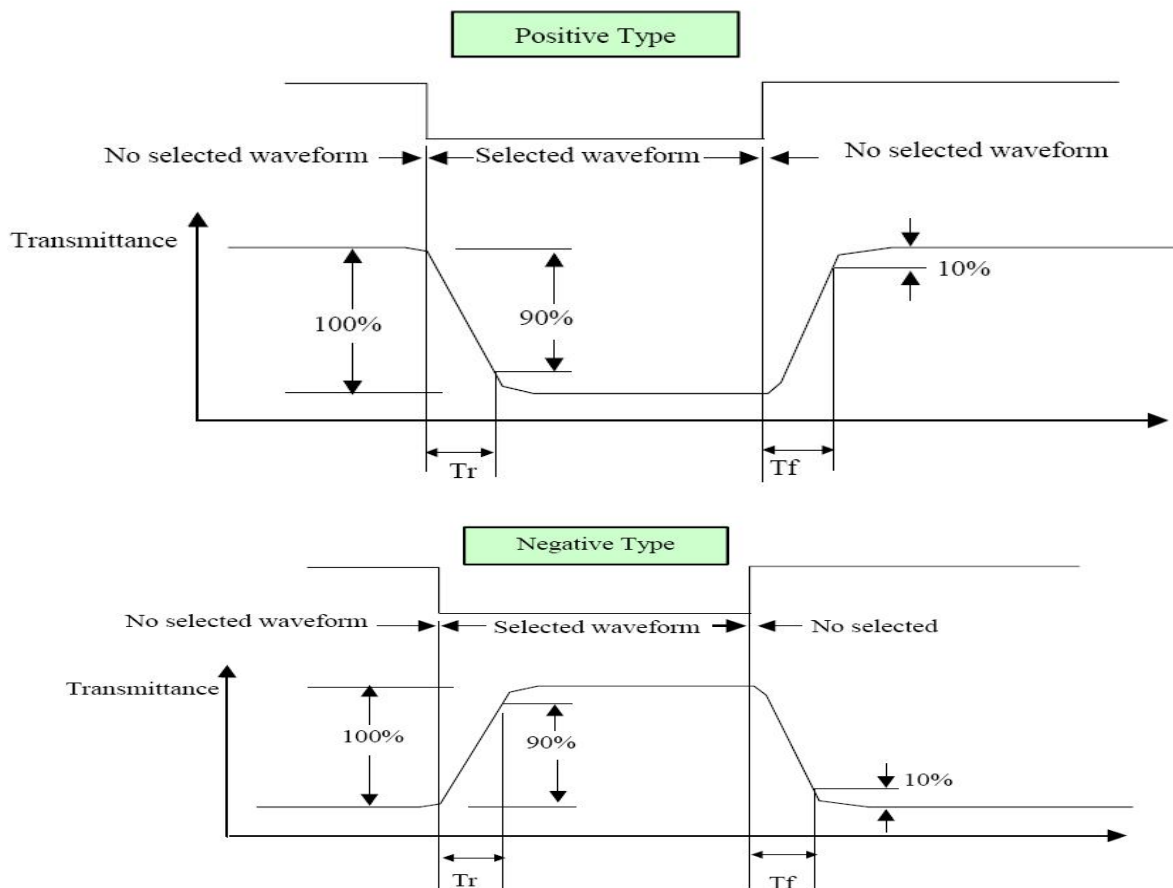
1. Measure surrounding : dark room;
2. Ambient temperature: $25\pm 2^\circ\text{C}$;
3. 30min.warm-up time.

Note Definition:

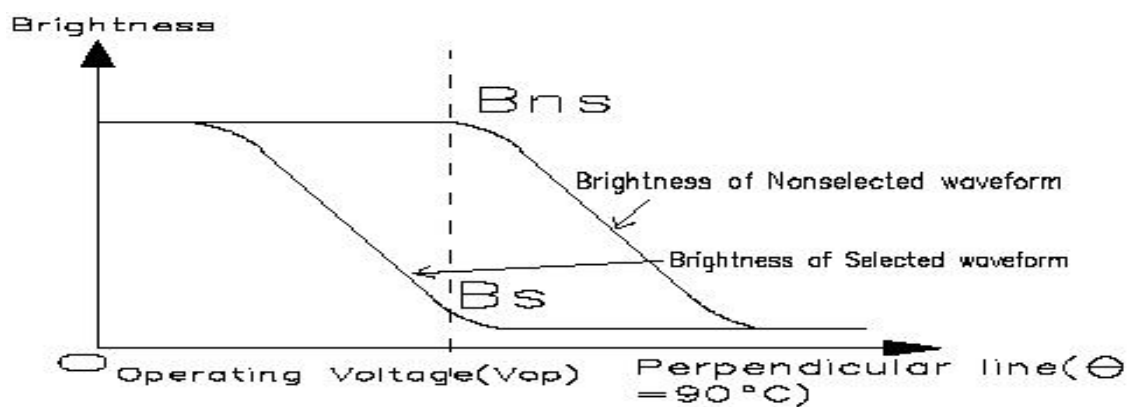
Note(1)Viewing angle range:



Note(2)Response Time:

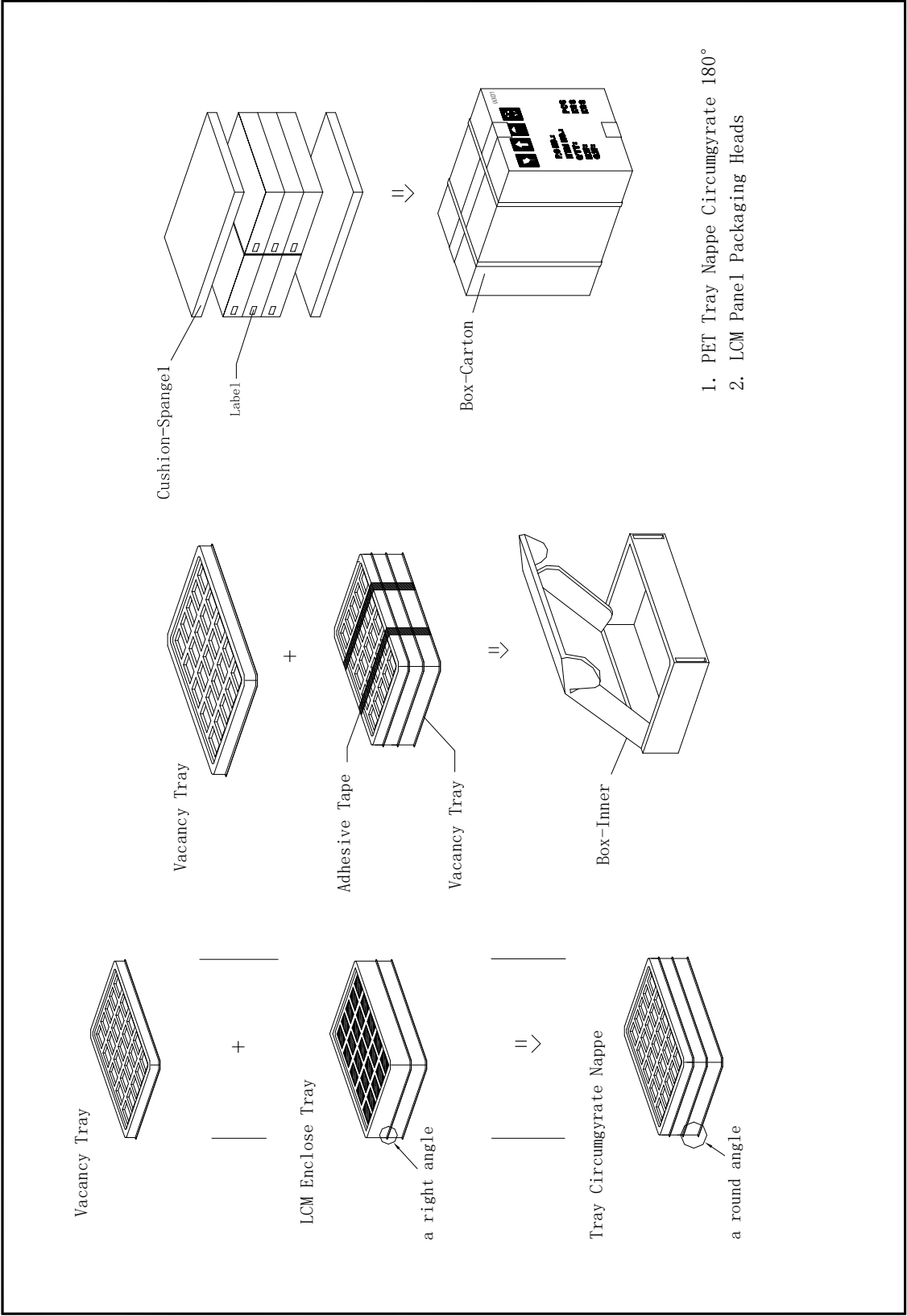


Note(3)Contrast Ratio Definition:



$$\text{Contrast Ratio (Cr)} = \frac{\text{Luminance with all pixel white}}{\text{Luminance with all pixel black}}$$

9.PACKAGE.



10. STANDARD SPECIFICATION FOR RELIABILITY:

Item	Condition		Time (hrs)	Assessment
High temp. Storage	85°C		120	No abnormalities in functions and appearance
High temp. Operating	85°C		120	
Low temp. Storage	-30°C		120	
Low temp. Operating	-30°C		120	
Humidity	60°C/ 90%RH		120	
Thermal Shock Temp. Cycle	-30°C → ←85°C (0.5hour → ←0.5 hour)		10cycles	
ESD Testing	HBM:	±8KV		330Ω/150PF
	MM:	±200V		200PF/0Ω

Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($25\pm10^{\circ}\text{C}$), normal humidity ($45\pm20\%$ RH), and in area not exposed to direct sun light. (Life time of backlight, please refer to Data about backlight.)

Testing Conditions and Inspection Criteria:

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in up Table, Standard specifications for Reliability have been executed in order to ensure stability.

Item	Test Model	In section Criteria
Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
Appearance	Visual inspection	Defect free.

11.SPECIFICATION OF QUALITY ASSURANCE:

11.1 Purpose

This standard for Quality Assurance should affirm the quality of LCD Module products to supply.

11.2 Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to MIL-STD105E.General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 2.5

Total defects: AQL = 2.5

11.3. Nonconforming Analysis & Deal With Manners

a. Nonconforming Analysis:

(i) Purchaser should supply the detail data of non- conforming sample and the non- conforming.

(ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.

(iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.

b. Disposition of nonconforming:

(i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.

(ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

11.4. Agreement items

Both sides should discuss together when the following problems happen.

a. There is any problem of standard of quality assurance, and both sides think that it must be modified.

b. There is any argument item which does not record in the standard of quality assurance.

c. Any other special problem.

11.5 Standard of The Product Appearance Test

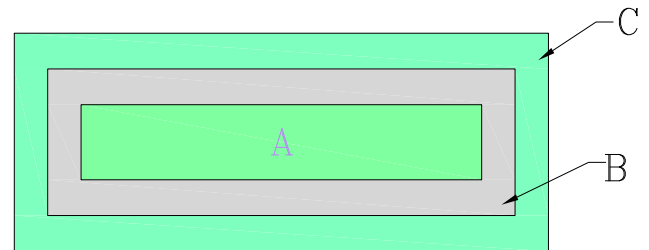
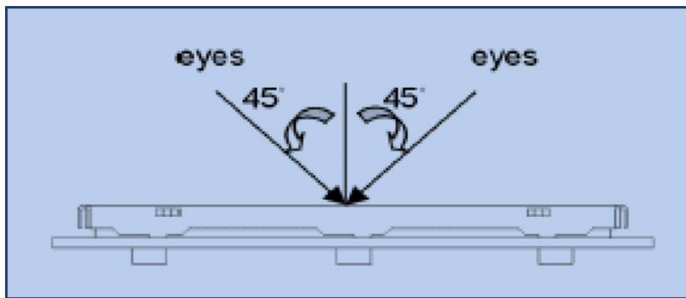
a. Manner of appearance test: This specification should be applied for both light on and off situation.

(i) The test must be under $20W \times 2$ or $40W$ fluorescent light, and the distance of view must be at $30 \pm 5cm$.

(ii) When test the model of transmissive product must add the reflective plate.

(iii)The test direction is base on about around 10° of vertical line (Left graph)

(iii)Temperature: $25 \pm 5^\circ\text{C}$ Humidity: $65 \pm 10\%\text{RH}$



(iv) Definition of area (Right graph)

A. Area: Viewing area. B. Area: Out of viewing area.(Outside viewing area)

b. Basic principle:

(i) It will accord to the AQL when the standard can not be described.

(ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.

(iii) Must add new item on time when it is necessary.

c. Standard of inspection: (Unit: mm)

Allowable limits defined in follow Dot defect Table should be met for each white, black , R, G, B raster. The limits apply to the entire area. Missing white in 60% or more of typical (one color, R or G or B) pixel aperture is defined as a bright defect, less than 60% is acceptable .Black spot in 60% or more of typical pixel aperture is defined as a dark defect, less than 60% is acceptable.

Dot defect table:

Item		White dot defect	Black dot defect	Total
1	Defect counts	3	3	3
2	Combined defect Counts	No combined dot defect allowed. Two Single dot defect that within 5mm during each dot defect should becouned as combined dot defect.		

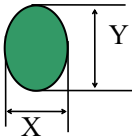
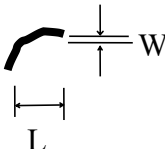
11.6 Inspection specification

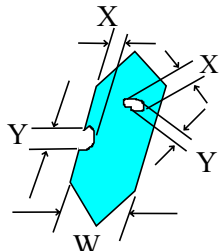
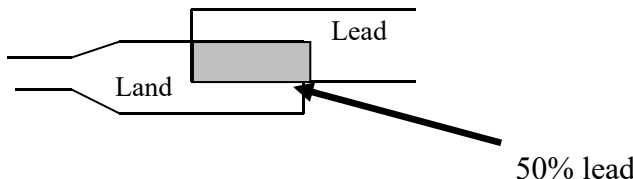
AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling

Classify	Item		Note	AQL	
Major	Display state	Short or open circuit	1	0.65	
		Contrast defect (dim, ghost)			
		LC leakage			
		Flickering			
		No display			
		Wrong viewing direction	2		
		Wrong Back-light	7		
	Non-display	Flat cable or pin reverse	9		
		Wrong or missing component	10		
Minor	Display state	Background color deviation	2	2.5	
		Black spot and dust	3		
		Line defect	4		
		Scratch			
		Rainbow	5		
		Pin hole	6		
	Polarizer	Bubble and foreign material	3		
		Scratch	4		
	PCB,FPC	Scratch	4		
	Soldering	Poor connection	8		
	Wire	Poor connection	9		
	LCD	CHIP OUT	11		

Note on defect classification:

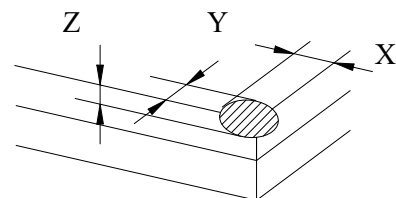
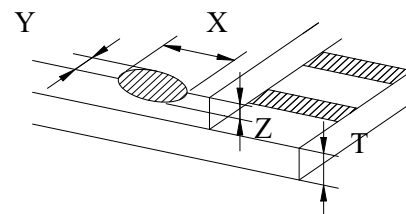
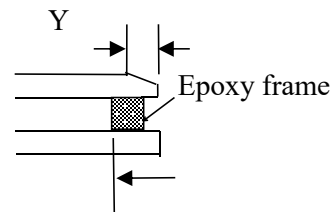
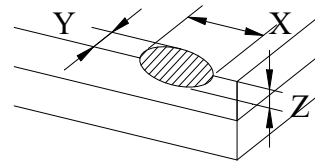
No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (incl. Polarizer) ex.: dirt under polarizer, Pinhole of reflector ,glass scratch, dirt under glass,scratch on polarizer $\phi = (X+Y)/2$	 <table><tr><td>Point</td><td>Acceptable Qty.</td></tr><tr><td>Size</td><td></td></tr><tr><td>$\phi \leq 0.20$</td><td>Disregard</td></tr><tr><td>$0.20 < \phi \leq 0.25$</td><td>3</td></tr><tr><td>$0.25 < \phi \leq 0.30$</td><td>2</td></tr><tr><td>$\phi > 0.30$</td><td>0</td></tr></table> Unit: mm	Point	Acceptable Qty.	Size		$\phi \leq 0.20$	Disregard	$0.20 < \phi \leq 0.25$	3	$0.25 < \phi \leq 0.30$	2	$\phi > 0.30$	0								
	Point	Acceptable Qty.																				
Size																						
$\phi \leq 0.20$	Disregard																					
$0.20 < \phi \leq 0.25$	3																					
$0.25 < \phi \leq 0.30$	2																					
$\phi > 0.30$	0																					
4	Line defect	 <table><tr><td></td><td>Line</td><td>Acceptable Qty.</td></tr><tr><td>L</td><td>W</td><td></td></tr><tr><td>---</td><td>$0.015 \geq W$</td><td>Disregard</td></tr><tr><td>$3.0 \geq L$</td><td>$0.03 \geq W$</td><td rowspan="2">2</td></tr><tr><td>$2.0 \geq L$</td><td>$0.05 \geq W$</td></tr><tr><td>$1.0 \geq L$</td><td>$0.1 > W$</td><td>1</td></tr><tr><td>---</td><td>$0.05 < W$</td><td>Applied as point defect</td></tr></table> Unit: mm		Line	Acceptable Qty.	L	W		---	$0.015 \geq W$	Disregard	$3.0 \geq L$	$0.03 \geq W$	2	$2.0 \geq L$	$0.05 \geq W$	$1.0 \geq L$	$0.1 > W$	1	---	$0.05 < W$	Applied as point defect
	Line	Acceptable Qty.																				
L	W																					
---	$0.015 \geq W$	Disregard																				
$3.0 \geq L$	$0.03 \geq W$	2																				
$2.0 \geq L$	$0.05 \geq W$																					
$1.0 \geq L$	$0.1 > W$	1																				
---	$0.05 < W$	Applied as point defect																				
5	Rainbow	Not more than two color changes across the viewing area																				

No.	Item	Criterion								
6	Segment pattern W = Segment width $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10\text{mm}$ is acceptable.  <table border="1"> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> <tr> <td>$\phi \leq 1/4W$</td> <td>Disregard</td> </tr> <tr> <td>$1/4W < \phi \leq 1/2W$</td> <td>1</td> </tr> <tr> <td>$\phi > 1/2W$</td> <td>0</td> </tr> </table> Unit: mm	Point Size	Acceptable Qty	$\phi \leq 1/4W$	Disregard	$1/4W < \phi \leq 1/2W$	1	$\phi > 1/2W$	0
Point Size	Acceptable Qty									
$\phi \leq 1/4W$	Disregard									
$1/4W < \phi \leq 1/2W$	1									
$\phi > 1/2W$	0									
7	Back-light	(1) The color of backlight should correspond its specification. (2) Not allow flickering								
8	Soldering	(1) Not allow heavy dirty and solder ball on PCB or FPC. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. 								
9	Wire	(1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.								
10	PCB,FPC	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.								

11

LCD

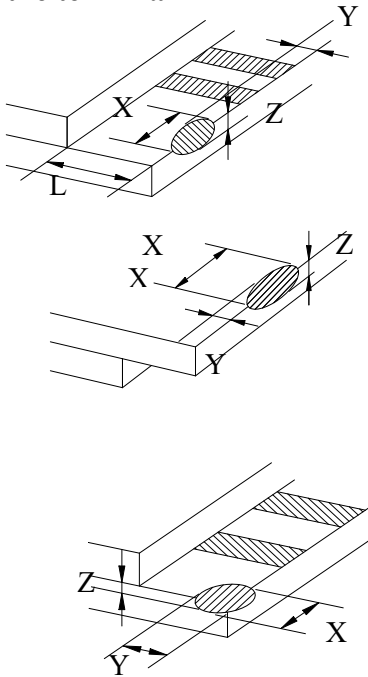
2.1.1 chip on the surface



Note: A: LCD Length

X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	Not enter into epoxy frame	$\leq T$
	Not enter into the inner edge of epoxy	$\leq 1/2T$

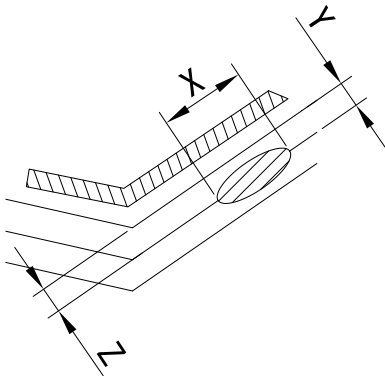
2.1.2 Chip on the terminal



X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/2L$	$\leq T$
$\leq 1/8A \& \leq 1\text{mm}$	$\leq L$	$\leq T$
$\leq 1/8A \& \leq 2\text{mm}$	$\leq L$	$\leq 1/2T$

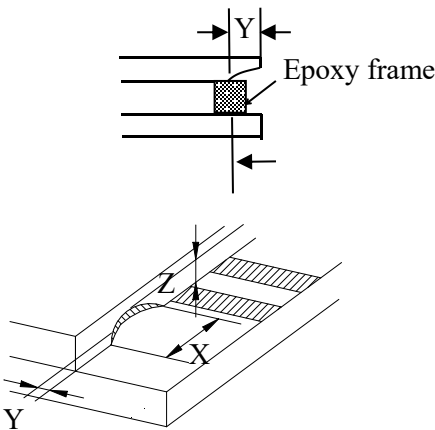
Note: A:LCD Length.
the distance between crack and contact pad must be greater than the width of 1st contact pad.

2.1.3 Chip out on between side



11

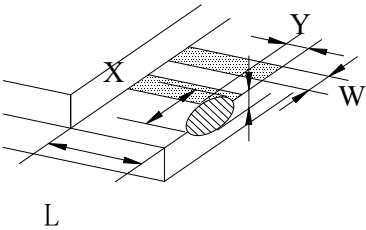
LCD



X	Y	Z
$\leq 1/8A$	Not enter into epoxy frame	$Z \leq 2T$
	Not enter into 1/2 epoxy frame	$Z \leq 1/2T$

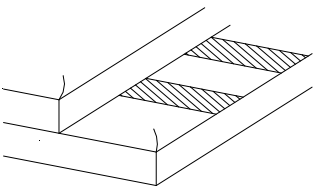
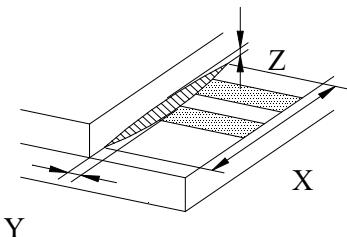
Note: A : LCD Length

2.1.4 including corner chip and side chip



Note: A:LCD Length

X	Y	Z
$>1/8A$	$\leq 1/6L$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/3L$	
$\leq 1/4W$	$\leq 2/3L$	

11	LCD	2.2 Chip out  1) Chip out is that crackles extend to inner edge. 2) Crackles round epoxy frame will be rejected. 3) Chip out on the terminal will be rejected: $Z=T$ length $>1\text{mm}$ or $Z<T$ length $>2\text{mm}$ 4) The chip out at ITO will be rejected.							
		2.3 Poor cutting  <table border="1"> <thead> <tr> <th>X</th><th>Y</th><th>Z</th></tr> </thead> <tbody> <tr> <td>$>1/8$ A</td><td>≤ 0.3</td><td>$\leq 1/2T$</td></tr> <tr> <td>$\leq 1/8$ A</td><td>According to drawing</td><td>$1/2T \leq Z \leq T$</td></tr> </tbody> </table> Note : A: LCD Length.	X	Y	Z	$>1/8$ A	≤ 0.3	$\leq 1/2T$	$\leq 1/8$ A
X	Y	Z							
$>1/8$ A	≤ 0.3	$\leq 1/2T$							
$\leq 1/8$ A	According to drawing	$1/2T \leq Z \leq T$							
12	SMT	According to the <Acceptable of electronic assemblies> IPC-A-610C class 2 stander. Component missing or function defect are Major defect ,the others are Minor defect.							

Any one out of the specification will be rejected.

12. GENERAL PRECAUTIONS

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Ketone
- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- As light dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal

open circuit.

Usage under the relative condition of 40 °C, 50%RH or less is required.

(6) Storage

In the case of storing for a long period of time (for instance, for years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is.

Keeping temperature in the specified storage temperature range.

- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol which should be burned up later.
- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

Hello Lighting co., ltd reserves the right to change this specification.

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- END -